



US006014745A

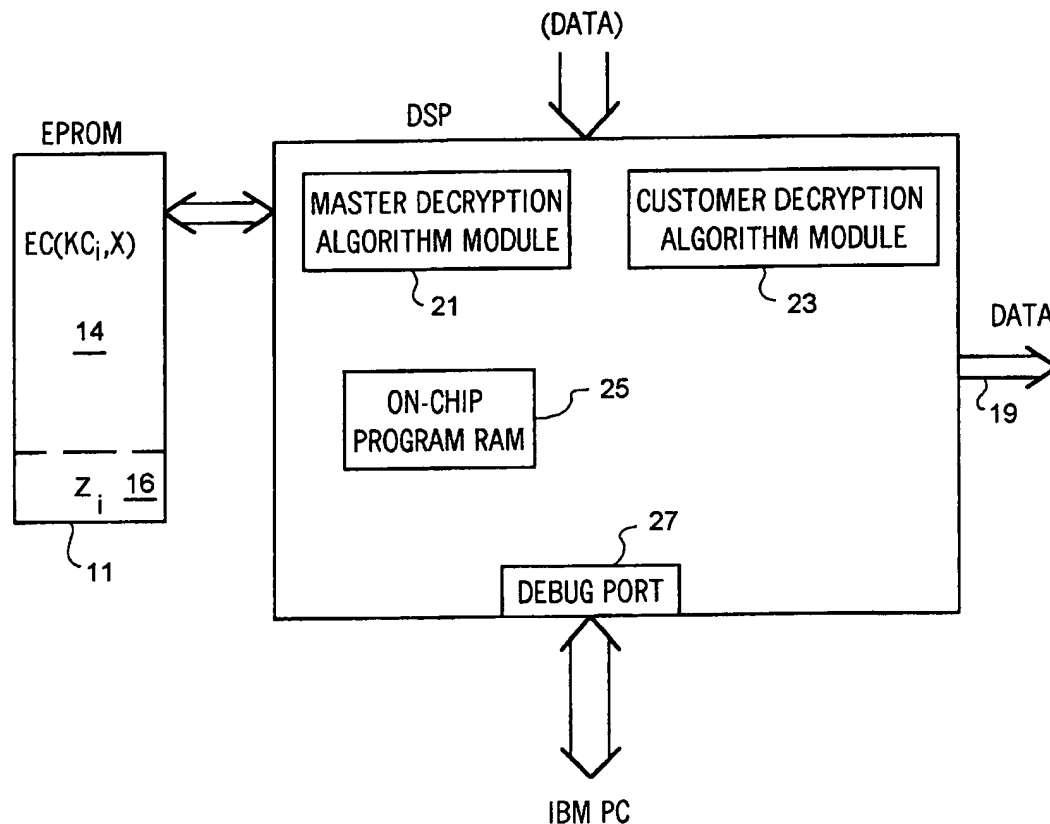
**United States Patent** [19]

Ashe

[11] **Patent Number:** 6,014,745[45] **Date of Patent:** Jan. 11, 2000[54] **PROTECTION FOR CUSTOMER PROGRAMS (EPROM)**5,604,800 2/1997 Johnson et al. .  
5,764,762 6/1998 Kazmierczak et al. .... 380/4[75] **Inventor:** Vincent Ashe, Co. Sligo, Ireland*Primary Examiner*—Bernarr E. Gregory  
*Attorney, Agent, or Firm*—Richard K. Robinson[73] **Assignee:** Silicon Systems Design Ltd., Dublin, Ireland[57] **ABSTRACT**[21] **Appl. No.:** 08/896,183[22] **Filed:** Jul. 17, 1997[51] **Int. Cl.<sup>7</sup>** ..... H04L 9/00; H04L 9/08[52] **U.S. Cl.** ..... 713/193; 713/189; 713/200;  
380/277; 380/278[58] **Field of Search** ..... 235/380; 380/4;  
380/9, 21, 23, 28, 49, 50, 59, 277, 278;  
713/189, 193, 200[56] **References Cited****U.S. PATENT DOCUMENTS**

4,105,156	8/1978	Dethloff .	
4,238,854	12/1980	Ehrsam et al. ....	380/4
4,295,041	10/1981	Ugon .	
4,382,279	5/1983	Ugon .	
4,764,959	8/1988	Watanabe et al. .	
5,343,530	8/1994	Viricel .	
5,563,947	10/1996	Kikinis .....	380/4
5,596,639	1/1997	Kikinis .....	380/4

Proprietary information such as programs and/or data are protected using a secure processing system that includes a memory such as an EPROM in which the proprietary information is stored in a first portion of the memory. The stored information is encrypted by an encryption algorithm unique to the proprietor of the information. The stored information is assigned an unique key that will enable the information to be decrypted if the unique algorithm is known. A second portion of the memory has the unique key stored therein, the unique key is encrypted with a master encryption algorithm and can only be obtained by using a master key with the master algorithm. A processing unit such as a Digital Signal Processor (DSP) has both the proprietor's unique algorithm, master algorithm and master key available to it. The processing unit can retrieve the information from the memory and execute it after decrypting the unique key using the master algorithm and the master key and then decrypting the information using the unique algorithm and the unique key.

**1 Claim, 4 Drawing Sheets**

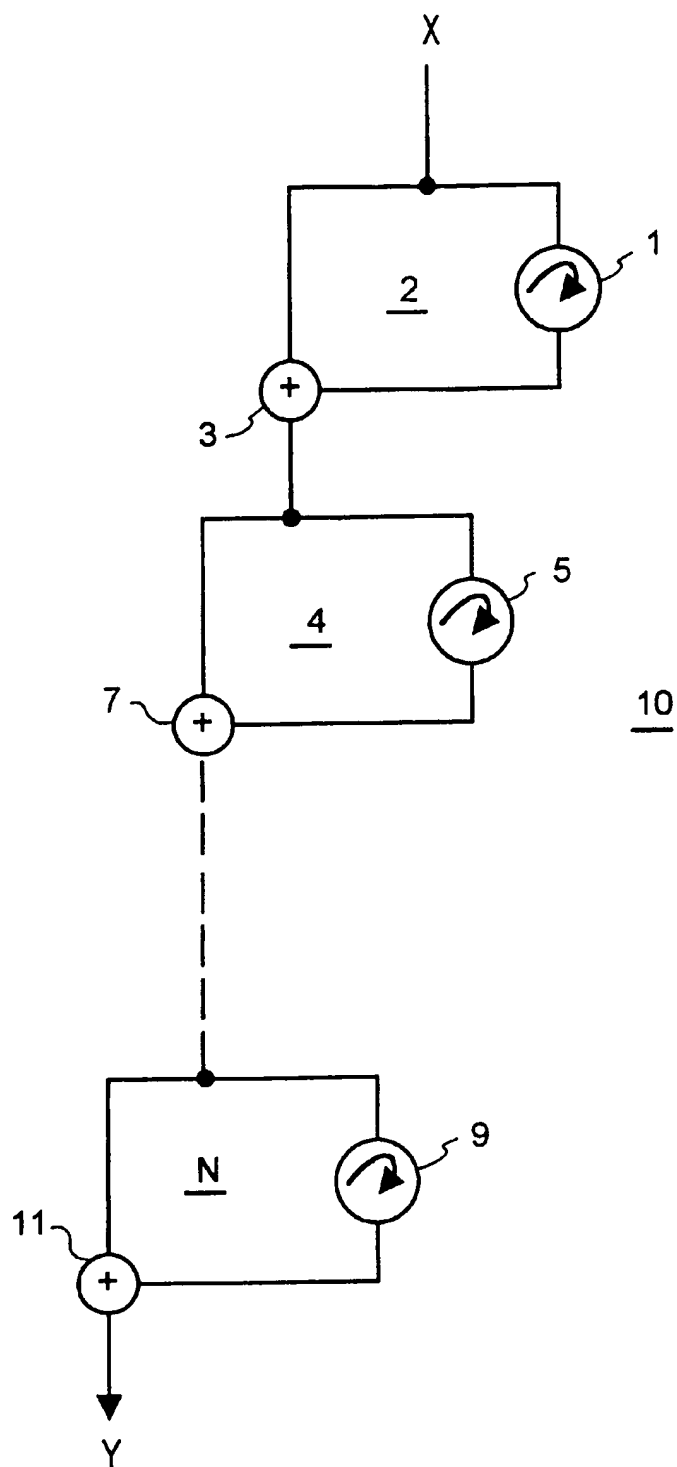


FIG. 1

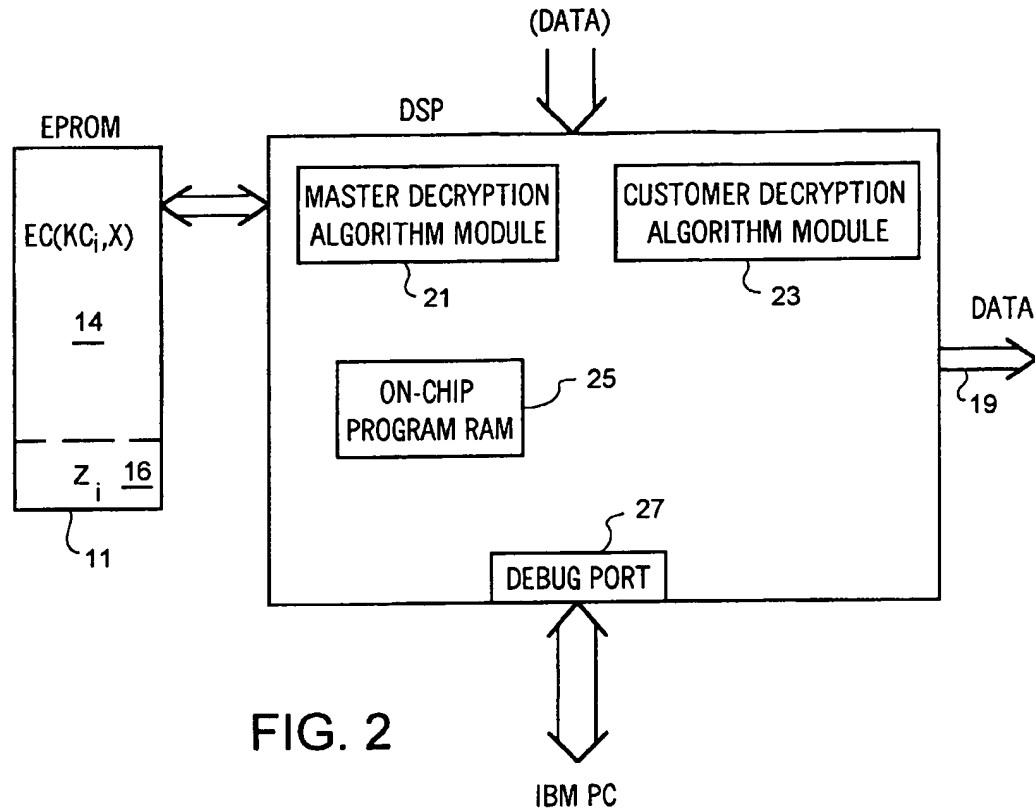


FIG. 2

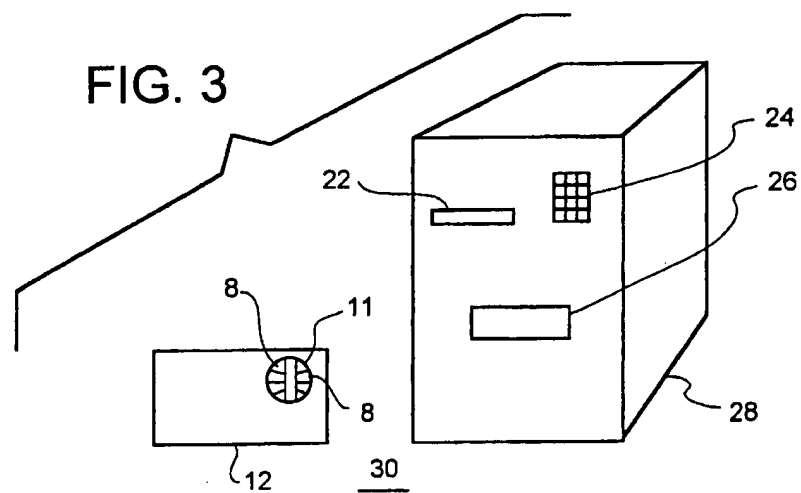


FIG. 3

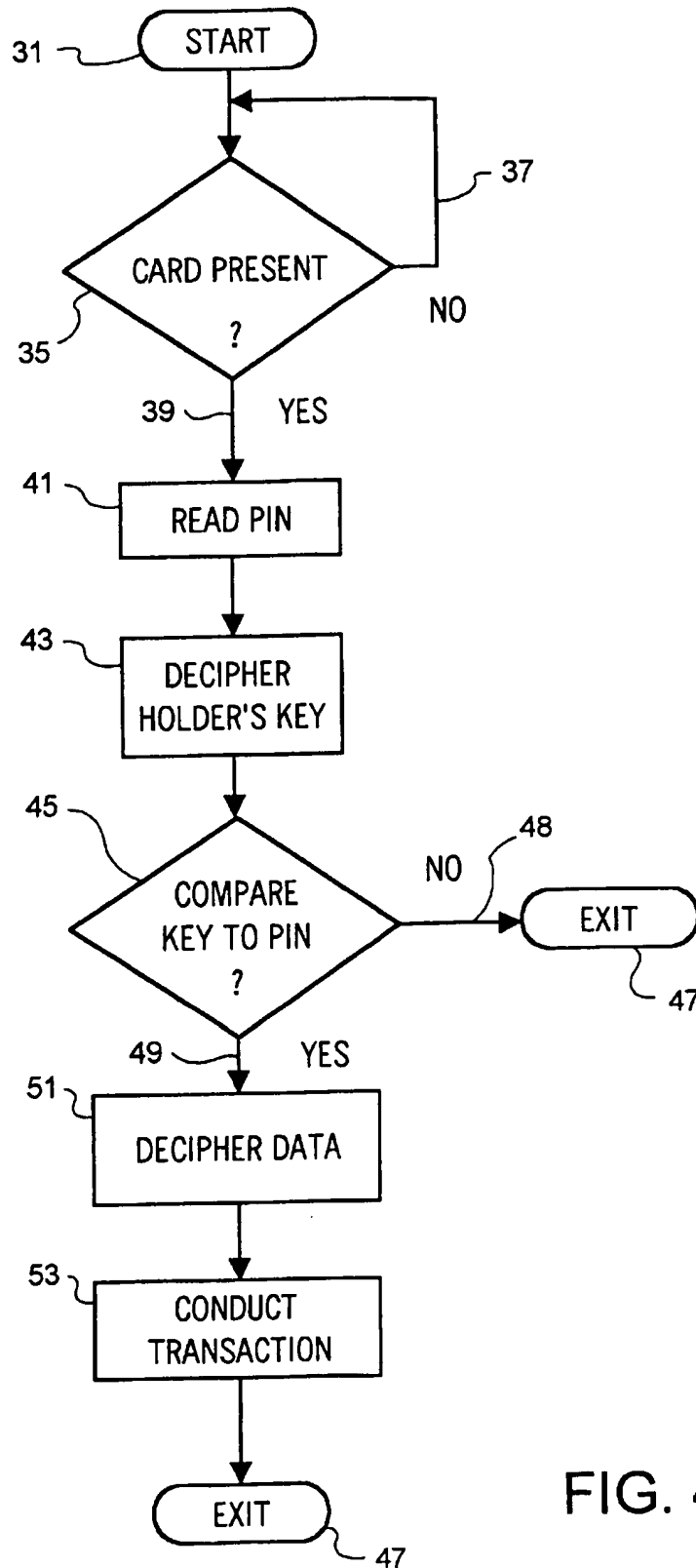


FIG. 4A

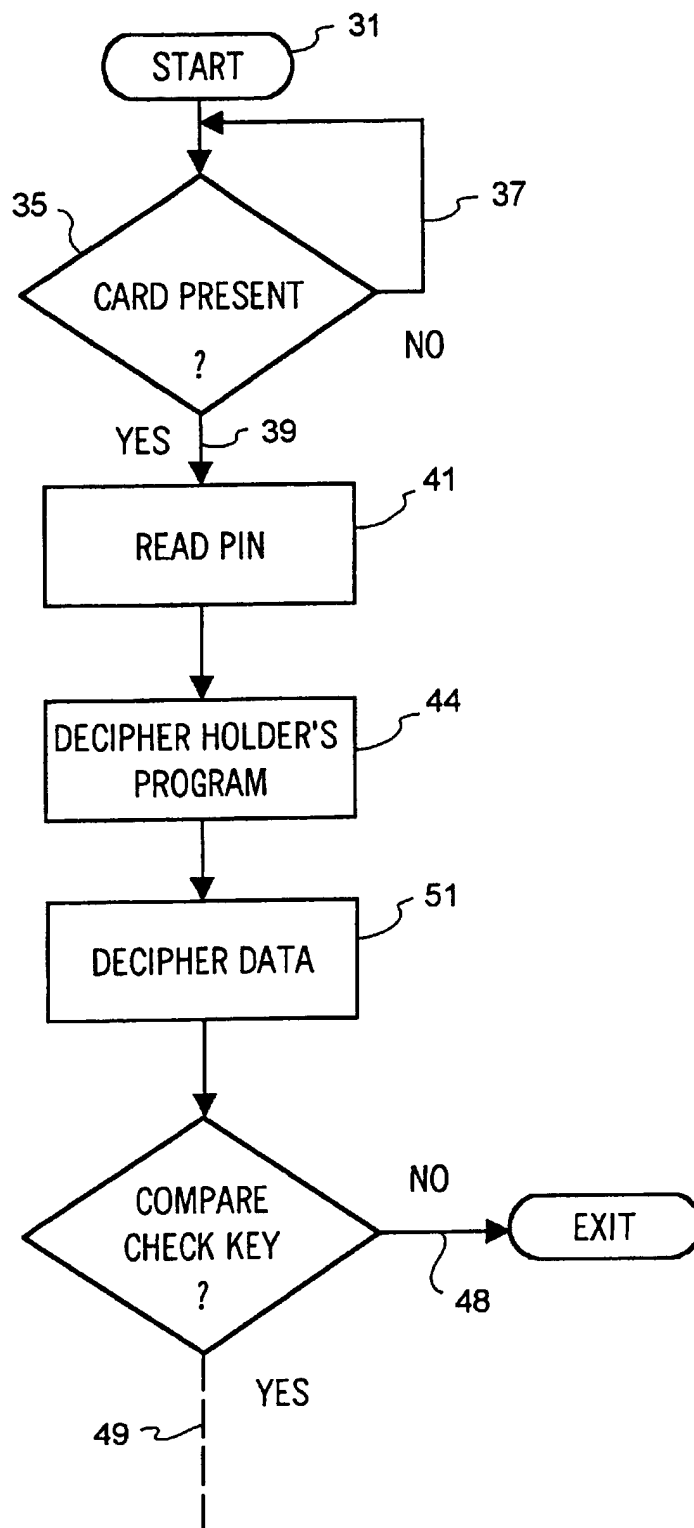


FIG. 4B

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## PROTECTION FOR CUSTOMER PROGRAMS (EPROM)

### BACKGROUND OF THE INVENTION

This invention relates to a method of protecting information stored in a memory device such as and EPROM.

The manufacturers of Digital Signal Processors (DSPs), as a service to their customers, will manufacture the DSP to operate with the customer's own proprietary programs. As a part of that service, the manufacturers attempt to protect the customer's proprietary information. In some cases, the proprietary information is stored external to the DSP in memories such as EPROMs, masked ROMs, CDs and even floppy discs. The DSPs are mass produced and are not unique to any particular customer. Any customer may develop a program to run on the DSP by using the DSP's unique OP-Codes. The concern that a customer has is that, after a substantial investment in time and money developing a proprietary program, a competitor or even a program pirate may obtain his program by purchasing a copy of it and a DSP and then extracting the program.

One way of protecting proprietary programs is to encrypt them prior to their being read. U.S. Pat. No. 4,764,959 disclosed a single-chip microcomputer with an encryption function that encrypts the contents of the memory for secrecy protection when the programs stored in a Read Only Memory (ROM) are read to the outside. However, it is quite simple to reverse engineer the ROM and determine the program by observing the state of each memory cell of the ROM.

Smart Card manufacturers also have developed many techniques for authenticating (a form of encryption) the validity of data stored in a smart card's memory. For example, U.S. Pat. No. 5,343,530 disclosed one method.

### SUMMARY OF THE INVENTION

Proprietary information such as programs and/or data are protected using a secure processing system that includes a memory, such as an EPROM, in which the proprietary information is stored in a first portion of the memory. The stored information is encrypted by an encryption algorithm unique to the proprietor of the information. The stored information is assigned a unique key that will enable the information to be decrypted if the unique algorithm is known. A second portion of the memory has the unique key stored therein. The unique key is encrypted with a master encryption algorithm and can only be obtained by using a master key with the master algorithm. A processing unit such as a Digital Signal Processor (DSP) has both the proprietor's unique algorithm, master algorithm and master key available to it. The processing unit can retrieve the information from the memory and execute it after decrypting the unique key using the master algorithm and the master key and then decrypting the information using the unique algorithm and the unique key.

The processing unit has an access port for accessing the information stored in the memory unit. A second processing unit, such as a personal computer, can be connected to the access port and an operator can enter the unique key. The second processing unit can then retrieve the encrypted information by providing the DSP with the unique key. The DSP will decipher the encrypted information using the proprietary algorithm and the unique key and then passes the decrypted programs and/or data to the second processing unit.

For security purposes, the master program is very complex but the unique algorithm is less complex than the master algorithm.

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The security system may also be used for smart cards. A smart card provider will have its own proprietary algorithm and key and each card holder will have their own unique key and algorithm such as a Personal Identification Number (PIN). The card holder will enter his card into a DSP device such as a cash machine and enter his PIN in the machine. The smart card has the PIN that has been encrypted by a master algorithm stored in a portion of the memory of the smart card. The DSP retrieves and deciphers the encrypted PIN and compares it to the PIN that the card holder had entered. If the comparison is correct, then the data stored in the smart card is transferred and deciphered in the manner discussed above. The card holder can then carry out the desired transaction.

### BRIEF DESCRIPTION OF THE FIGURES

FIG. 1 is a schematic diagram of an encryption algorithm;

FIG. 2 is a block diagram of the security system according to the invention; and,

FIG. 3 is a block diagram of an alternate embodiment of the invention.

FIGS. 4A and 4B are flow charts of the transaction.

### DETAILED DESCRIPTION OF THE INVENTION

FIG. 1 is a schematic diagram of a simplified encryption algorithm 10, which is provided for illustrative purposes. The encryption algorithm 10 has a plurality of stages 2, 4, . . . N. The larger the value of N or the greater the number of stages the more complex the encryption algorithm. Each stage has an operation such as 1 for stage 2, operation 5 for stage 4 and operation 9 for stage N. The operation shift the bit stored in that stage in a first direction for a defined number of bits or in a second direction for a defined number of bits depending upon the key that defines the shift direction and magnitude of the shift. The key word controls the operation including the direction and magnitude of the shift. The output of each stage is the combination of the operated bit with the unoperated bit. Thus, adder 3 combines x with x', the operated bit and applies it to stage 4 where operator 5 operates on it according to the key bit for that stage. Adder 7 combines x+x' with (x+x') which summation is applied to the next stage where the process is repeated. However, the operator for each stage is under the control of a key bit. The final stage N after completion of the operation by operator 9 and the summation by the adder 11 provides as an output Y which is the encryption of x by algorithm E according to key K or  $Y=E(K,x)$ .

FIG. 2 is a block diagram of a secure system according to the invention in which encrypted information Y is stored in first section 14 of a memory 11 such as an EPROM, masked ROM, Compact Disc (CD) or even a floppy disc. The memory 11 has a second section 16 in which an encrypted Key Zi is stored. In one embodiment the encrypted information is a proprietary program X developed to operate a DSP 13. The encrypted program Y is encrypted by a unique algorithm Ec and Key Kci known only to the proprietor of the program and the manufacturer of the DSP 13. In general, the algorithm Ec is selected for speed in order for the encrypted program Y to be rapidly deciphered by the DSP 13. Therefore, its complexity will be limited. To enhance the security of the system the Key Kc is encrypted by a complex algorithm Em known only to the manufacturer of the DSP with a Key Km also known only to the manufacturer of the DSP.

The DSP reads Zi, the encrypted Kc, from the memory 11 via connector 12 and deciphers it with the master decryption

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algorithm module 21. After the Key Kc is deciphered the DSP reads the encrypted program Y with the customer decryption algorithm module 23. The program is stored in the on-chip program RAM 25 and the DSP execute the program on data provided on input 17 and provides the modified data as an output on bus 19.

The program developer may want to debug the program so there is a debug port 27 provided. A personal computer PC may be connected to the debug port 27 and input the Key Kc to obtain a copy of the deciphered programmed.

As an alternative the algorithm Ec may be encrypted and stored in the section 16 and the Key retained in the DSP 13. The operation would be essentially the same as described above.

FIG. 3 is an alternate embodiment of the secured system of FIG. 2. In FIG. 3, a microprocessor (not shown) similar to DSP 13 is included as part of a smart card system 30. The memory 11 is mounted on a card 112 which is designed to be inserted into a slot 26. The memory has conductors 8 which, as known in the art (such as in U.S. Pat. No. 4,382,279 which is incorporated herein by reference) when placed into the slot 22 contact with contractors 32, allow a machine 28 such as a cash machine having a microprocessor mounted within it to read the memory 11. In this embodiment, the card holder enters his key via keypad 24. After verification that the entered key is the same as the encrypted key, the machine 28 will allow the card holder to conduct transactions and obtain cash via dispenser 26.

The flow chart for executing the above transactions is illustrated in FIG. 4A to which reference should now be made. After start at block 31, the microprocessor 113 checks to see if a card is present in the slot 22. If present, the microprocessor obtains the holders PIN a block 41 via the keypad 24 and the port 27 of machine 28. Following the

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entry of the PIN, the holders Key is deciphered using the Master decryption algorithm module 21 at block 43. If the PIN does not equal the Key then at decision block 45 the NO line 48 is taken to the program exits at exit 47. If the PIN is equal to the deciphered Key then the YES line 49 is taken and the encrypted information stored in the memory 11 is deciphered by the customer decryption algorithm module 23 at block 51. The transaction is completed at block 53 following which the system exits the program at block 47.

Finally, both the memory 11 and processing unit may be mounted on the same card. Utilizing the teachings of this invention, the embodiment of U.S. Pat. No. 4,382,279 disclosed the memory and processor on the same card, may be substantially improved by incorporating the security system disclosed herein.

I claim:

1. A method of protecting information, comprising:
  - encrypting the information by using a first algorithm and a first key,
  - storing the encrypted information in a first portion of a memory unit;
  - encrypting the first key with a second algorithm and second key;
  - storing the encrypted first key in a second portion of the memory unit;
  - retrieving the encrypted first key with a processing unit having stored therein the first algorithm, the first key and the second algorithm;
  - decrypting the first key using the second algorithm and the second key; and
  - decrypting the information using the first algorithm and the first key.

\* \* \* \* \*

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO : 6,014,745  
DATED : January 11, 2000  
INVENTOR(S): Vincent Ashe

It is certified that error appears in the above-identified patent and that said Letters Patent  
is hereby corrected as shown below:

In column 4, line 8, please replace "ad" and  
substitute therefor -- and --.

Signed and Sealed this  
Twenty-sixth Day of December, 2000

Attest:



Q. TODD DICKINSON

Attesting Officer

Director of Patents and Trademarks



UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,014,745  
DATED : January 11, 2000  
INVENTOR(S) : Vincent Ashe

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, line 27, please replace "first key" and substitute therefor  
— second key — .

Signed and Sealed this  
Twentieth Day of March, 2001



Attest:

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Acting Director of the United States Patent and Trademark Office